

A Versatile eMMC Programmer from a Vendor You can trust!

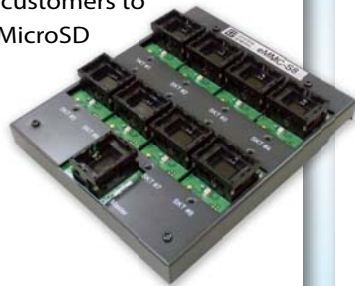
Product Highlights



- Supports eMMC spec. versions: v4.3, v4.4, v4.41 and v4.5
- Supports Smart Mode, Partition Mode and Clone Mode
- Supports Chip Erase and Quick Erase
- Supports 3.3V, 1.8V & 1.2V eMMC devices
- Supports System General eMMC Image Builder
- Windows software interface and available "Job Control" functions
- Real-time statistics displays
- Friendly user interface
- Programming speed up to 36MB per second
- Allows being chained up to 20 programmer sites via USB ports for manual operations; automated operations available on System General Handlers
- Gang-programs up to 8 eMMC devices on each programmer site
- Socket cards hot-swappable for different package types without re-starting the programming system
- High-speed Slave duplications through the Master eMMC or MicroSD
- Flexible software architecture designed to accommodate custom application requirements

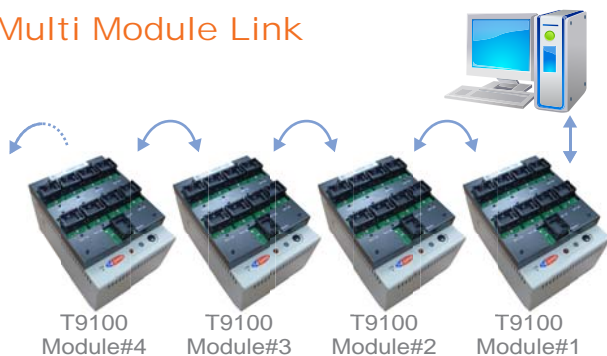
Introduction

eMMC-S8 is a socket board designed to duplicate eMMC devices under the T9100 dedicated platform! The socket board is made of one Master socket card and up to 8 Slave socket cards, and is able to perform high-speed copy and verification from Master to blank Slaves. In case the Master is not available and customers have to rely on "Image Files", an eMMC "Image Builder" software from System General will assist customers on converting the files into a System General custom format. The converted format file is then programmed into a blank MicroSD by using the available MicroSD Card Writer to generate the Master MicroSD. The eMMC-S8 socket board allows customers to use either Master eMMC or Master MicroSD to duplicate eMMC Slaves.



Various Socket Board Designs

Multi Module Link



Programming Mode

Although the eMMC software architecture was designed to accommodate custom application requirements, most applications follow the eMMC specifications and the following three programming modes will meet customers' standard eMMC application needs:

- Smart Mode
The system starts up by scanning through the Master eMMC and logging those non-blank areas on a system-created "Scan Table". Once Slave duplications are executed, the system will follow the Scan Table to do Copy/Verify from Master to blank Slaves.
- Partition Mode
The system will follow the scanned "Partition Table" in the Master eMMC to execute Copy/Verify on blank Slaves.
- Clone Mode
The system will copy/verify the Master to blank Slaves from the beginning address to the ending address, regardless of the data pattern in the Master.



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T9100
eMMC Programming System

General Specifications

- Product Description..... Support eMMC V4.3, V4.4, V4.41, V4.5; optional Upgrade Pack to extend supports to Memory and Logic devices
- Module Multi-Link..... Up to 20 sites
- Number of Sockets per Module..... 1 to 4 sockets, 1 to 8 sockets
- Operation Mode User-definable asynchronous or synchronous operation
- Pin-Drivers..... 112 Analog Pin Drivers
- Pin-Drivers Outputs..... VCCP, VPP, VPE, VPS, VIH, VIL, ZH, ZL, Gnd (range: 0 ~ 21.0V)
- Hi-Voltage DAC..... 3 sets (all current-mode)
- RAM Buffer..... 8192 Mbits standard (expandable to 16384 Mbits)
- Communication Interface..... USB 2.0 (Windows 2000/XP/7 required)

Environmental Requirements

- Operating temperature.....5~45°C (41~113°F)
- Operating humidity.....90% non-condensing

Standard Accessories

- Power cord
- Power connection cable
- System software CD (on-line help and tutorial)
- USB cable

Physical Specifications

- Dimensions..... 20.0 x 14.5 x 10.3 cm (7.9 x 5.7 x 4.1 inch)
- Shipping dimensions... 37.5 x 17.0 x 26.0 cm (14.8 x 6.7 x 10.2 inch)
- Weight..... 2.40 kg (5.33 lbs)
- Shipping weight..... 3.53 kg (7.84 lbs)

Electrical Requirements

- Operating voltage..... 100-240 VAC
- Frequency range..... 50/60 Hz
- Power consumption..... 40 VA max

PC System Requirements

- Operating system..... Windows 2000/XP/7
- Processor..... Pentium 4 or above
- Free disk space..... 3GB recommended
- DRAM..... 512MB recommended
- Communication..... USB 2.0 recommended

Option

Devices Upgrade Pack: Supports PLD, CPLD, FPGA, NAND, NOR, MCU devices

Enhanced Job Management Control and Statistics Report

The screenshot displays the 'Session Report' window of the T9100 Device Programmer. It shows a task named 'SanDisk' for a 'SanDisk SDHC2-4G' device. The report includes a 'Session Report' table with columns for Site(s), Total, Pass, Fail, IOP, CIF, BCF, IBCF, EF, PF, VF, and HFV. The overall statistics show a Pass Count of 00000058 (96.7%), a Fail Count of 00000002 (3.3%), and a Total Count of 00000060. A 'Fail statistics' table on the right lists various error codes like ICF, BCF, IBCF, EF, PF, VF, HFV, LVF, SF, PHF, LPHF, and OF with their respective counts and percentages.



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